



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

Confirmation No.: 5415

GOTOH et al.

Art Unit: 2857

Serial No.: 10/082,055

Examiner: D. Raymond

Filed: February 26, 2002

Docket No.: 100021-00074

For: TEST CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT  
EFFECTIVELY CARRYING OUT VERIFICATION OF CONNECTION  
OF NODES

**PETITION FOR EXTENSION OF TIME**

Director to the U.S. PTO  
P.O. Box 1450  
Alexandria, VA 22313-1450

November 17, 2004

Sir:

Applicants petition the Commissioner of Patents and Trademarks to extend the time for response to the Office Action dated May 17, 2004 for three (3) months from September 17, 2004 until November 17, 2004.

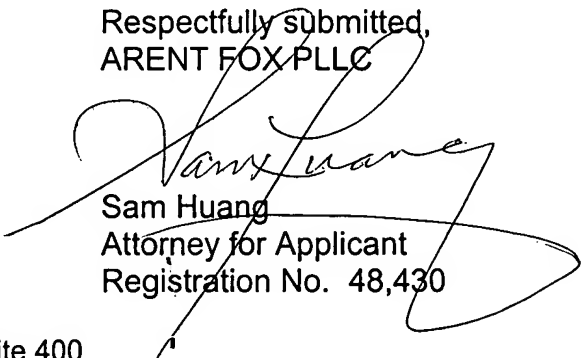
A check in the amount of Nine Hundred and Eighty Dollars (\$980.00) to cover the cost of the extension fee is enclosed. The Commissioner is hereby authorized to charge any deficiency or credit any overpayment associated with this communication to Deposit Account No. 01-2300 referencing Attorney Docket No.100021-00074.

11/18/2004 AADDF01 00000049 10082055

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980.00 OP

Respectfully submitted,  
ARENT FOX PLLC

  
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